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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/721,225	11/26/2003	Joern Luetzen	INF 2004 SP 00115	5694
48154 7590 04/29/2008 SLATER & MATSIL.L.P			EXAM	IINER
17950 PREST		GOUDREAU, GEORGE A		
SUITE 1000 DALLAS, TX	75252		ART UNIT	PAPER NUMBER
,			1792	
			MAIL DATE	DELIVERY MODE
			04/29/2008	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Application No. Applicant(s) 10/721,225 LUETZEN ET AL. Office Action Summary Examiner Art Unit George A. Goudreau 1792 -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --Period for Reply A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS. WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any

earned patent term adjustment. See 37 CFR 1.704(b).

1)🛛	Responsive to communica	ation(s) filed on 17 August 2007.
2a)⊠	This action is FINAL.	2b) This action is non-final.
21	Other will be a small and the factor	

Status

3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.
isposit	ion of Claims
5)□ 6)⊠ 7)□	Claim(s) 1-13 and 21-35 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. Claim(s) is/are allowed. Claim(s) 1-13 and 21-35 is/are rejected. Claim(s) is/are objected to.
	Claim(s) are subject to restriction and/or election requirement.
9)□ 10)□	The specification is objected to by the Examiner. The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.
riority ı	ınder 35 U.S.C. § 119
a)	Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). All b Some * c None of: 1. Certified copies of the priority documents have been received. 2. Certified copies of the priority documents have been received in Application No. 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). See the attached detailed Office action for a list of the certified copies not received.

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Attachment(s)		
Notice of References Cited (PTO-892)	4) Interview Summary (PTO-413)	
Notice of Draftsperson's Patent Drawing Review (PTO-948)	Paper No(s)/Mail Date	
3) Information Disclosure Statement(s) (PTO/S6/08)	5). Notice of Informal Patent Application.	
Paper No/s VMail Date	6) Other:	

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 Applicant's arguments with respect to claims of record have been considered but are moot in view of the new ground(s) of rejection.

 Claims 1, and 22-35 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

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-In claim 1, the preamble of the claim is not commensurate in scope with the body of the claim. (i.e.-The preamble of the claim recites a method for making an integrated circuit while the body of the claim fails to recite a method for making an integrated circuit. It is therefore unclear to the examiner whether this feature is claimed since it does not appear in the body of the claim.); and

-In claim 28, the preamble of the claim is not commensurate in scope with the body of the claim. (i.e.-The preamble of the claim recites a method for making an integrated circuit while the body of the claim fails to recite a method for making an integrated circuit. It is therefore unclear to the examiner whether this feature is claimed since it does not appear in the body of the claim.)

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The following is a quotation of the appropriate paragraphs of 35
 U.S.C. 102 that form the basis for the rejections under this section made in this
 Office action:

A person shall be entitled to a patent unless -

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for a patent.

- (e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application field under the treaty defined in section 35(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.
- Claims 1-12, 21-28, and 30-35 are rejected under 35 U.S.C. 102(a/e) as being anticipated by Kudelka (6,566,273).

Kudelka discloses a process for etching oval trenches in a cz-Si wafer using a rectangular grid which is aligned with its x, and y axis parallel to the 110 crystal plane which etches at a faster rate than the 100 crystal plane does. In the process bottle shaped openings are formed inside the trench which has the sidewalls expanded below regions of the rectangular grid. Kudelka uses an ammonium hydroxide solution to anisotropically wet etch the Si to form the bottle shaped openings in the cz-Si wafer. The etched trenches are used to form storage capacitors which are part of a DRAM structure. This is discussed in columns 1-10. This is shown specifically in figures 8-16; and shown in general in figures 1-16.

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 The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

- (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior and are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).
- Claims 13, and 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over the reference as applied in paragraph 4 above.

The reference as applied in paragraph 4 above fails to disclose the specific formation of the transistor portion of the DRAM structure out of selection transistors.

It would have been obvious to one skilled in the art to use selection transistors to form the transistor portion of the DRAM structure in the device which is fabricated above based upon the following. The usage of selection transistors to form the transistor portion of a DRAM structure in a device is

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conventional or at least well known in the DRAM arts. (The examiner takes

is taught above to the specific means which are taught above.

official notice in this regard.) Further, this simply an alternative, and at least equivalent means for forming the transistor portion of the DRAM structure which

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, THIS ACTION IS MADE FINAL.
 See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

 Any inquiry concerning this communication should be directed to examiner George A. Goudreau at telephone number 571-272-1434.

/George A. Goudreau/ Primary Examiner, Art Unit 1792